AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

LISTING OF CLAIMS

1.   (Previously Presented)   A method of arranging a host apparatus to transmit commands to an external storage medium device connected to the host apparatus over an external databus which is arranged in accordance with one of the IEEE 1394 standard or the Universal Serial Bus standard, the method comprising:

providing the host apparatus with a command bus and a command interface arranged in accordance with one of the ATA/IDE standard and the Serial ATA standard for transmitting commands to a storage medium device over the command bus; and

providing the host apparatus with at least one integrated circuit chip connected to the command bus and to the external databus and having an interface arranged to convert commands received from the command bus in a format in accordance with said one of the ATA/IDE standard and the Serial ATA standard into a format in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard and to transmit the converted commands over the external databus.

2.   (Original) A method according to claim 1, wherein the host apparatus is a digital television receiver apparatus.

3.   (Previously Presented) A method according to claim 1, wherein one of the ATA/IDE standard and the Serial ATA standard is the ATA/IDE standard.

4. (Previously Presented) A method according to claim 1, wherein said one of the IEEE 1394 standard and the Universal Serial Bus standard is the IEEE 1394 standard.

5. (Original) A method according to claim 4, wherein said one of the IEEE 1394 standard and the Universal Serial Bus standard is the IEEE 1394 standard including a Serial Bus Protocol.

6. (Previously Presented) A method according to claim 1, wherein the interface of the integrated circuit chip comprises: a first layer arranged in accordance with said storage medium device standard to receive commands from the command bus; a second layer arranged to convert commands output from the first layer into a format in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard; and a third layer arranged in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard to transmit the converted commands over the external databus.

7. (Currently Amended) A host apparatus arranged to transmit commands to an external storage medium device connected to the host apparatus over an external databus which is arranged in accordance with one of the IEEE 1394 standard and the Universal Serial Bus standard, the host apparatus comprising:

a command bus and a command interface arranged in accordance with one of the ATA/IDE standard and the Serial ATA standard for transmitting commands to a storage medium device over the command bus; and

at least one integrated circuit chip connected to the command bus and having terminals for connection to the external databus, the integrated circuit chip having an interface arranged to convert commands received from the command [[.]]bus in a format in accordance with one of the ATA/IDE standard and the Serial ATA standard into a format in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard, and to supply the converted commands to the terminals for connection to the external databus.

8. (Original) A host apparatus according to claim 7, wherein the host apparatus is a digital television receiver apparatus.

9. (Previously Presented) A host apparatus according to claim 7, wherein said one of the ATA/IDE standard and the Serial ATA standard is the ATA/IDE standard.

10.   (Previously Presented) A host apparatus according to claim 7, wherein said one of the IEEE 1394 standard and the Universal Serial Bus standard is the IEEE 1394 standard.

11.   (Original) A host apparatus according to claim 10, wherein said one of the IEEE 1394 standard and the Universal Serial Bus standard is the IEEE 1394 standard including a Serial Bus Protocol.

12.   (Previously Presented) A host apparatus according to claim 7, wherein the interface of the integrated circuit chip comprises: a first layer arranged in accordance with said one of the ATA/IDE standard and the Serial ATA standard to receive commands from the command bus; a second layer arranged to convert commands output from the first layer into a format in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard; and a third layer arranged in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard to transmit the converted commands over the external databus.

13.   (Previously Presented) An integrated circuit chip having:

terminals for connection to a command bus in accordance with one of the ATA/IDE standard and the Serial ATA standard for transmitting commands to a storage medium device over the command bus;

terminals for connection to an external databus in accordance with one of the IEEE 1394 standard and the Universal Serial Bus standard; and

an interface arranged to convert commands received at the terminals for connection to a command bus from a format in accordance with one of the ATA/IDE standard and the Serial ATA standard into a format in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard, and to supply the converted commands to the terminals for connection to the external databus.

14.   (Original) An integrated circuit chip according to claim 13, wherein the host apparatus is a digital television receiver apparatus.

15.   (Previously Presented) An integrated circuit chip according to claim 13, wherein one of the ATA/IDE standard and the Serial ATA standard is the ATA/IDE standard.

16.   (Previously Presented) An integrated circuit chip according to claim 13, wherein said one of the IEEE 1394 standard and the Universal Serial Bus standard is the IEEE 1394 standard.

17.   (Original)   An integrated circuit chip according to claim 16, wherein said one of the IEEE 1394 standard and the Universal Serial Bus standard is the IEEE 1394 standard including a Serial Bus Protocol.

18.   (Previously Presented)   An integrated circuit chip according to claim 13, wherein the interface of the integrated circuit chip comprises: a first layer arranged in accordance with one of the ATA/IDE standard and the Serial ATA standard to receive commands from the command bus; a second layer arranged to convert commands output from the first layer into a format in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard; and a third layer arranged in accordance with said one of the IEEE 1394 standard and the Universal Serial Bus standard to transmit the converted commands over the external databus.